

**MULTIFRAME 205**  
26 FRAMES, 120ms

Slots: 0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25

Slot 0 details:

- 1 slot = 156.25 bits
- 1 slot = 625 Qbits (quarter)
- 1 bit = 3.69 μs
- (1259 Obits) 576.875 μs 1 GSM Slot
- 1249 Obits 4.615 msec, 10,000 Obits 1 GSM TDMA Frame
- Guard time (66 Obits) 30.46 μs
- 9999 Obit

TDMA Frame Slots: Slot 7, Slot 0, Slot 1, Slot 2, Slot 3, Slot 4, Slot 5, Slot 6, Slot 7

Slot 0 internal structure:

- 215 Tail 3 bits
- 210 TCH 57 bits
- 225 Stealing bit
- 220 Midamble 26 bits
- 210 Stealing bit
- 215 TCH 57 bits
- 230 Tail 3 bits
- 230 235 guard bits

Slot 7	Slot 0 210a	Slot 1	Slot 2	210b Slot 3	Slot 4	Slot 5	Slot 6	Slot 7
--------	----------------	--------	--------	----------------	--------	--------	--------	--------





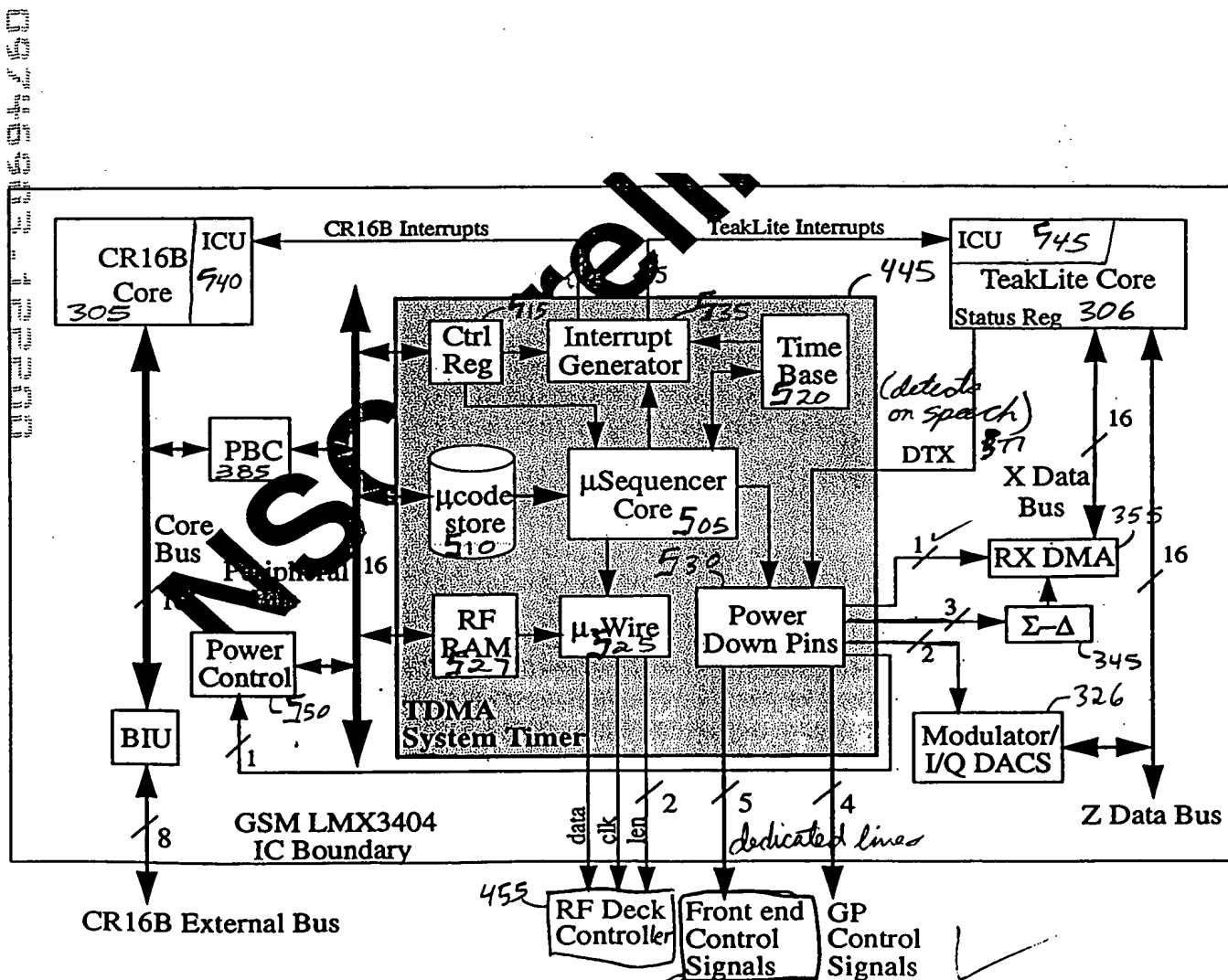


FIGURE 4

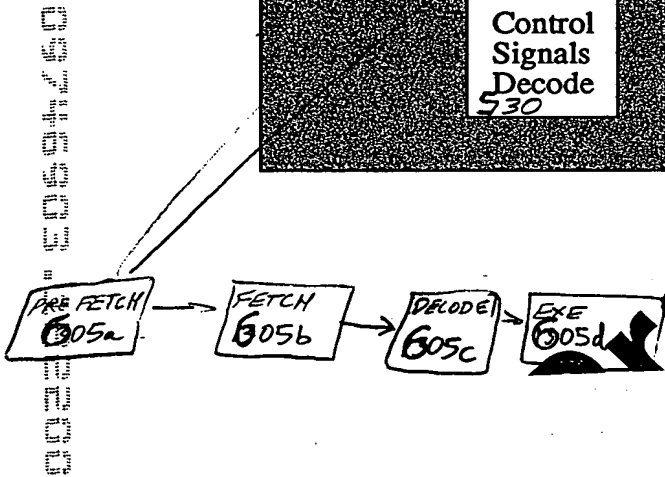
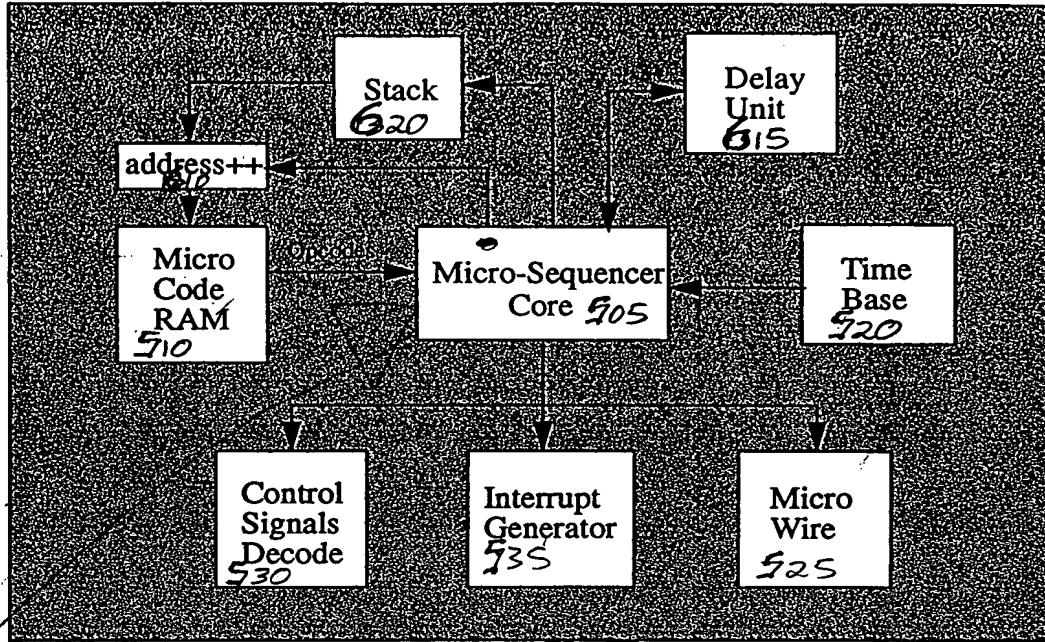


FIG. 5

T



FIGURE 6.

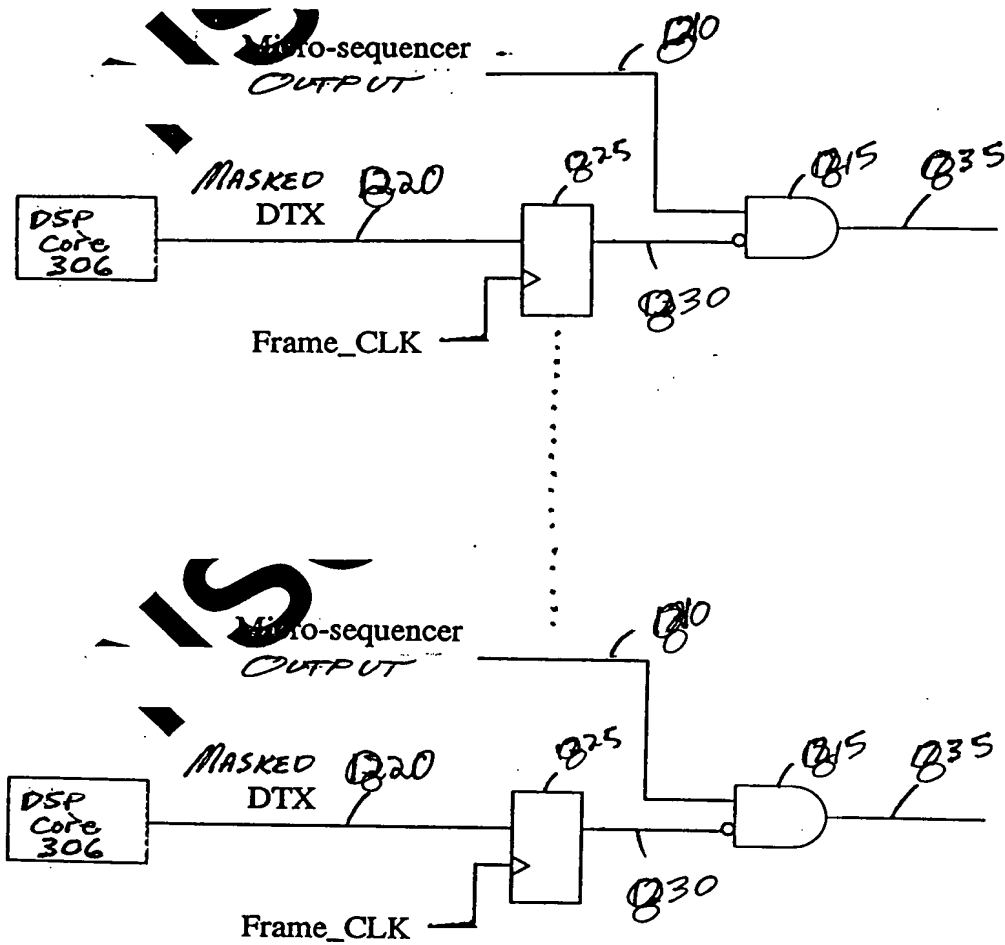


FIGURE ~~1218~~ 7

05746903.12200

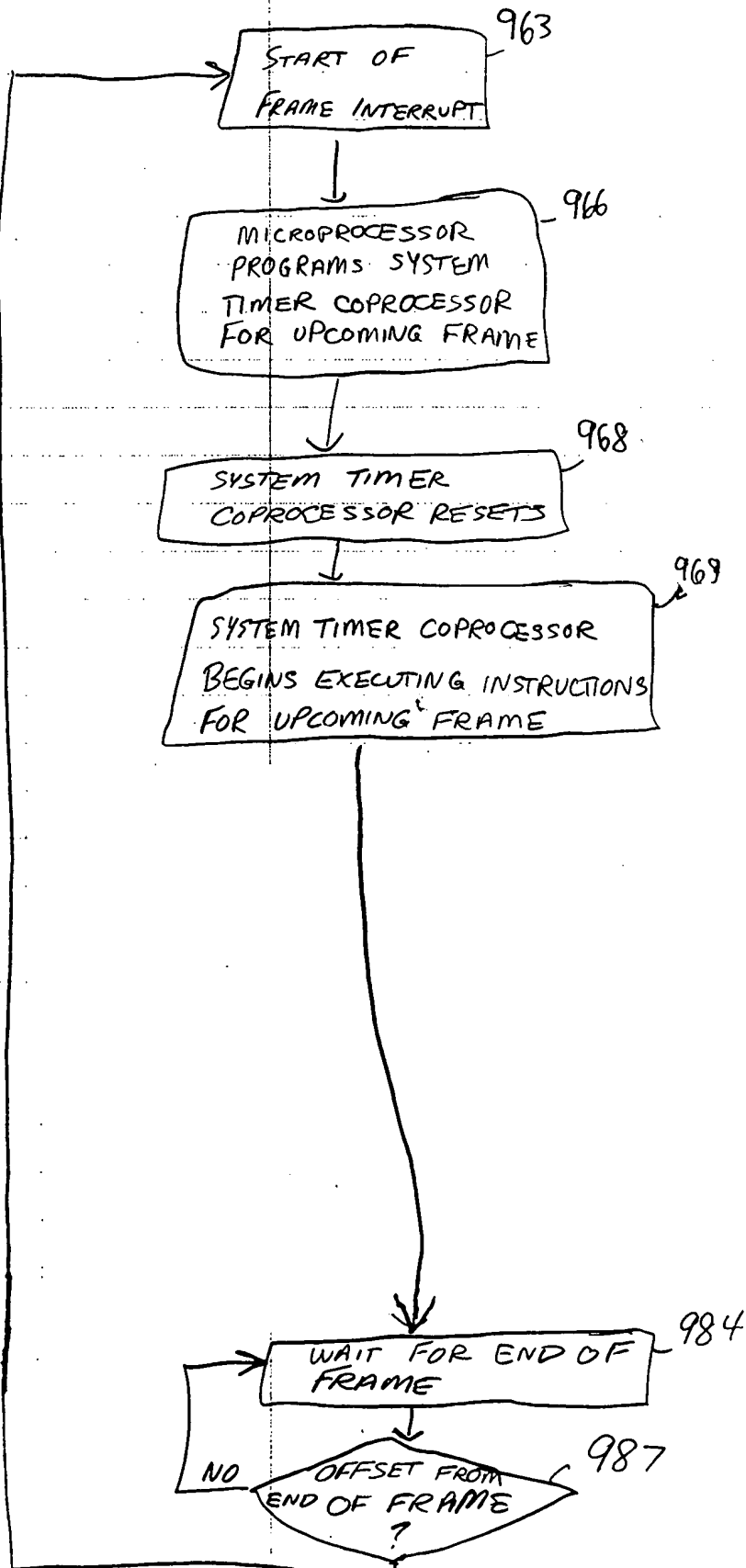
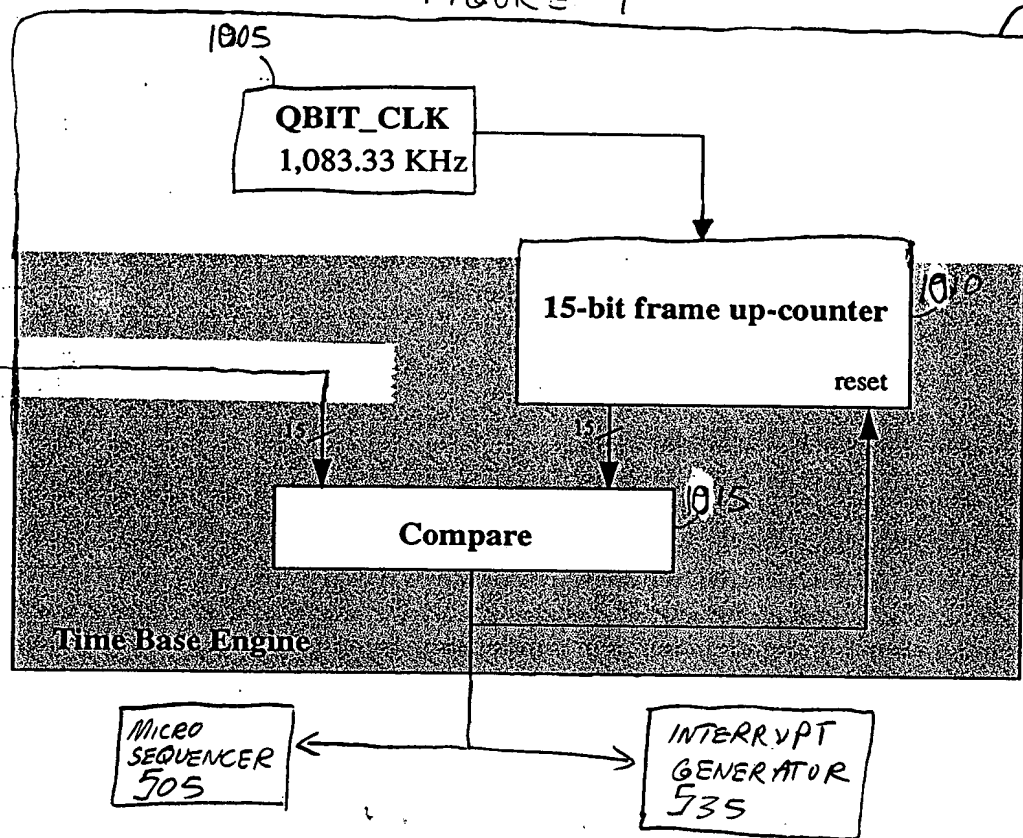


FIGURE 8

FIGURE 9

520



104

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AME TICK COUNT

**QBIT\_CLK**  
1,083.33 KHz

### 15-bit frame up-counter

**reset**

## Compare

## Time Base Engine

MICRO  
SEQUENCER  
505

INTERRUPT  
GENERATOR  
535



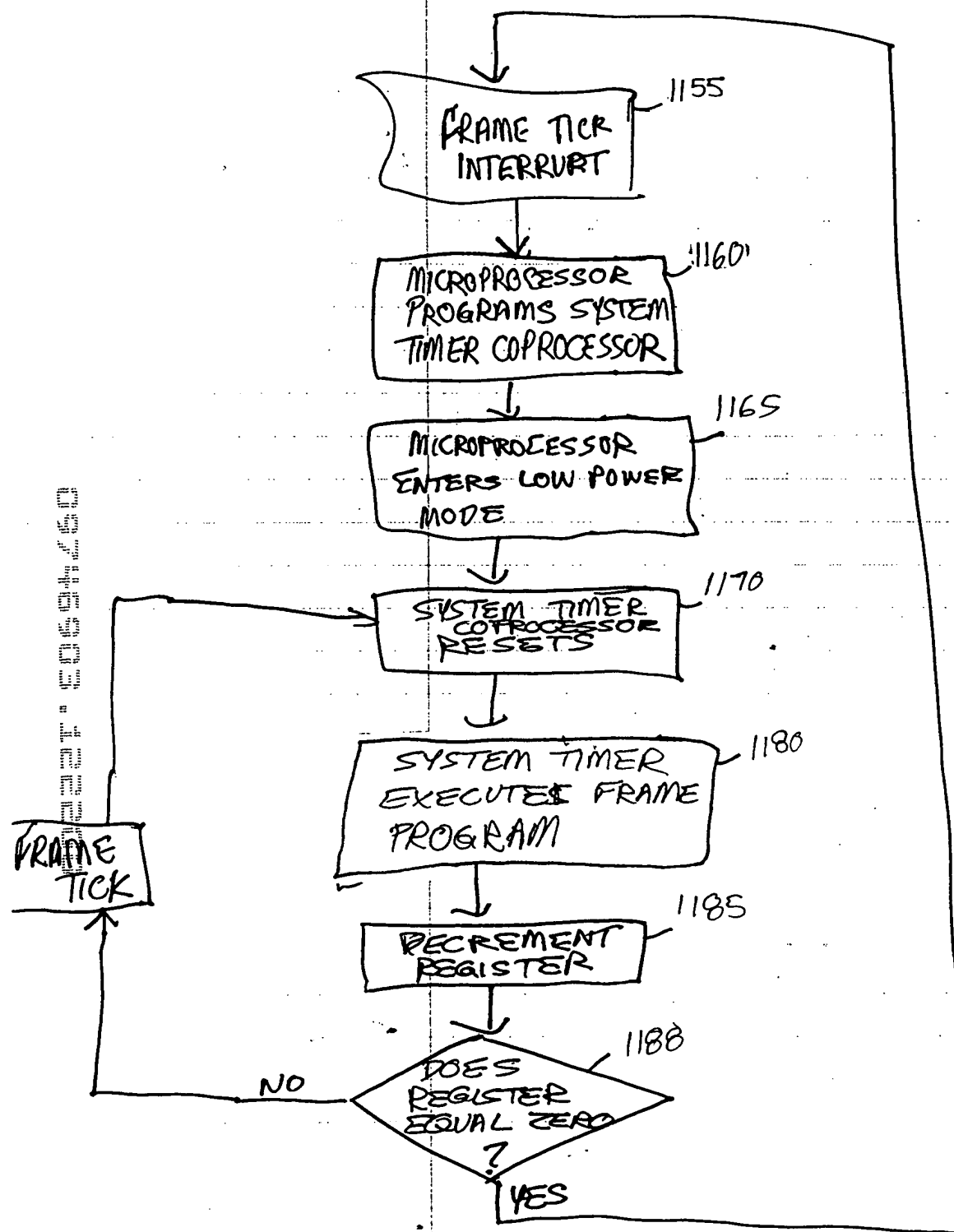


FIGURE ~~10~~ 10

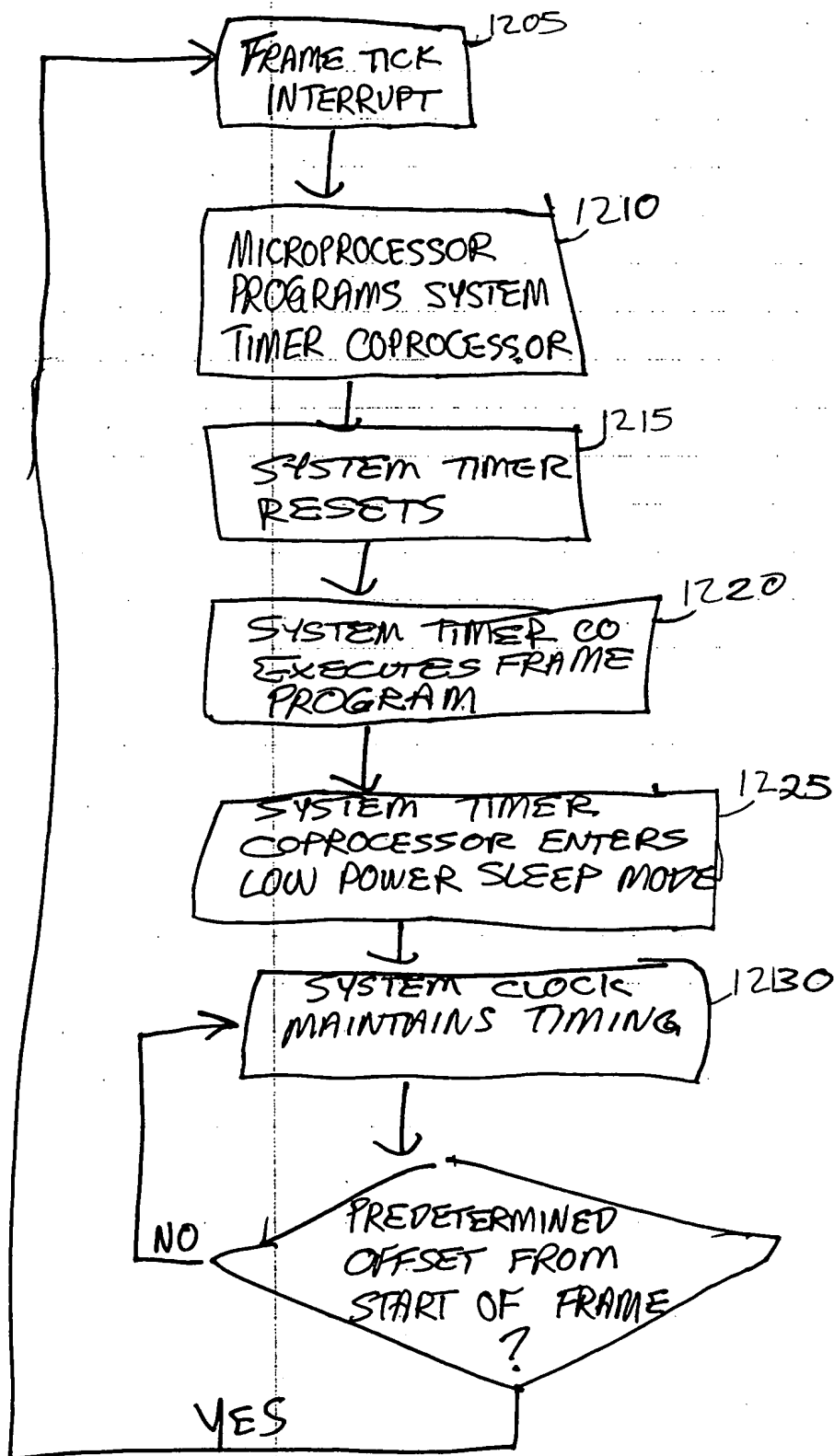


FIG. 11

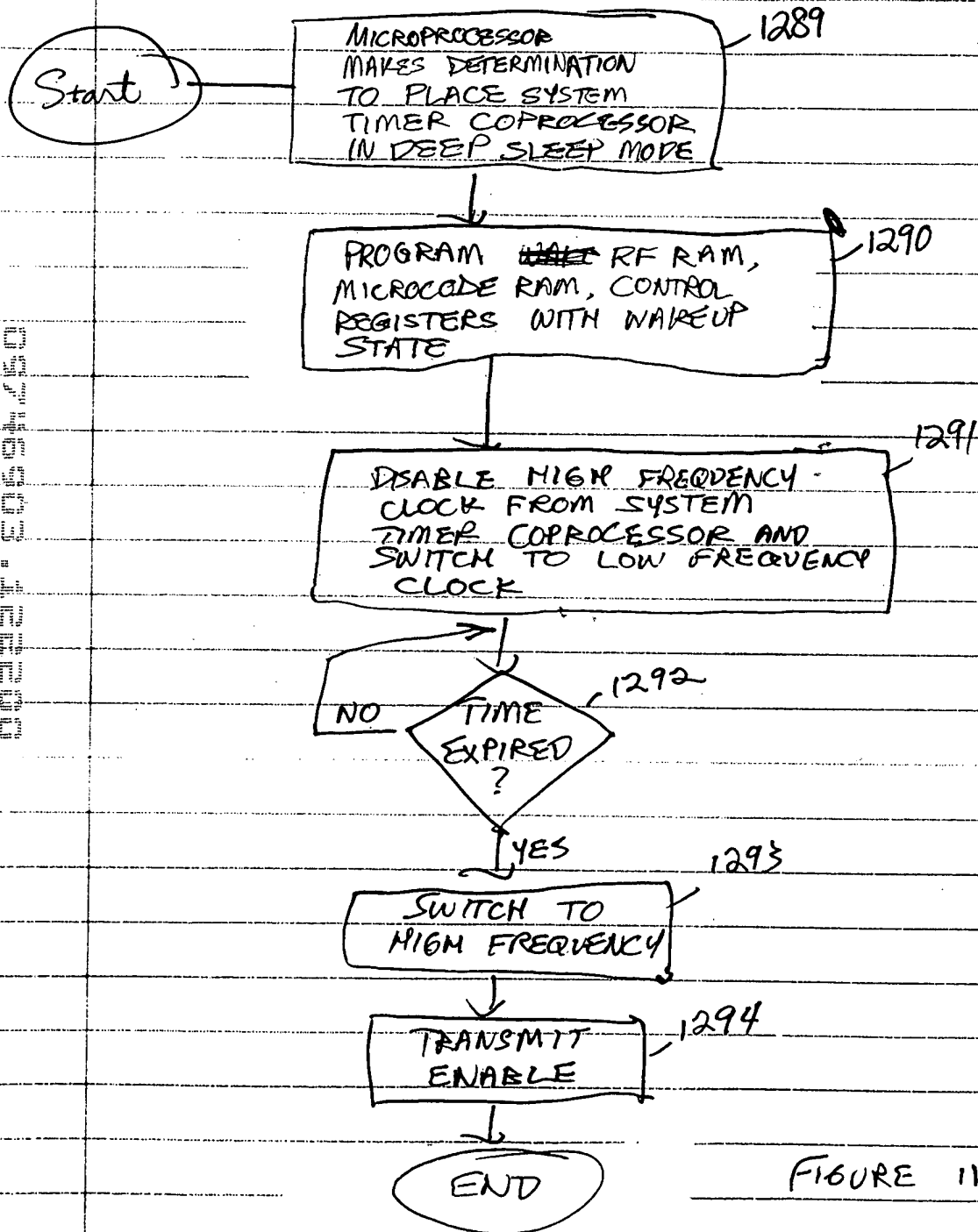


FIGURE 11B

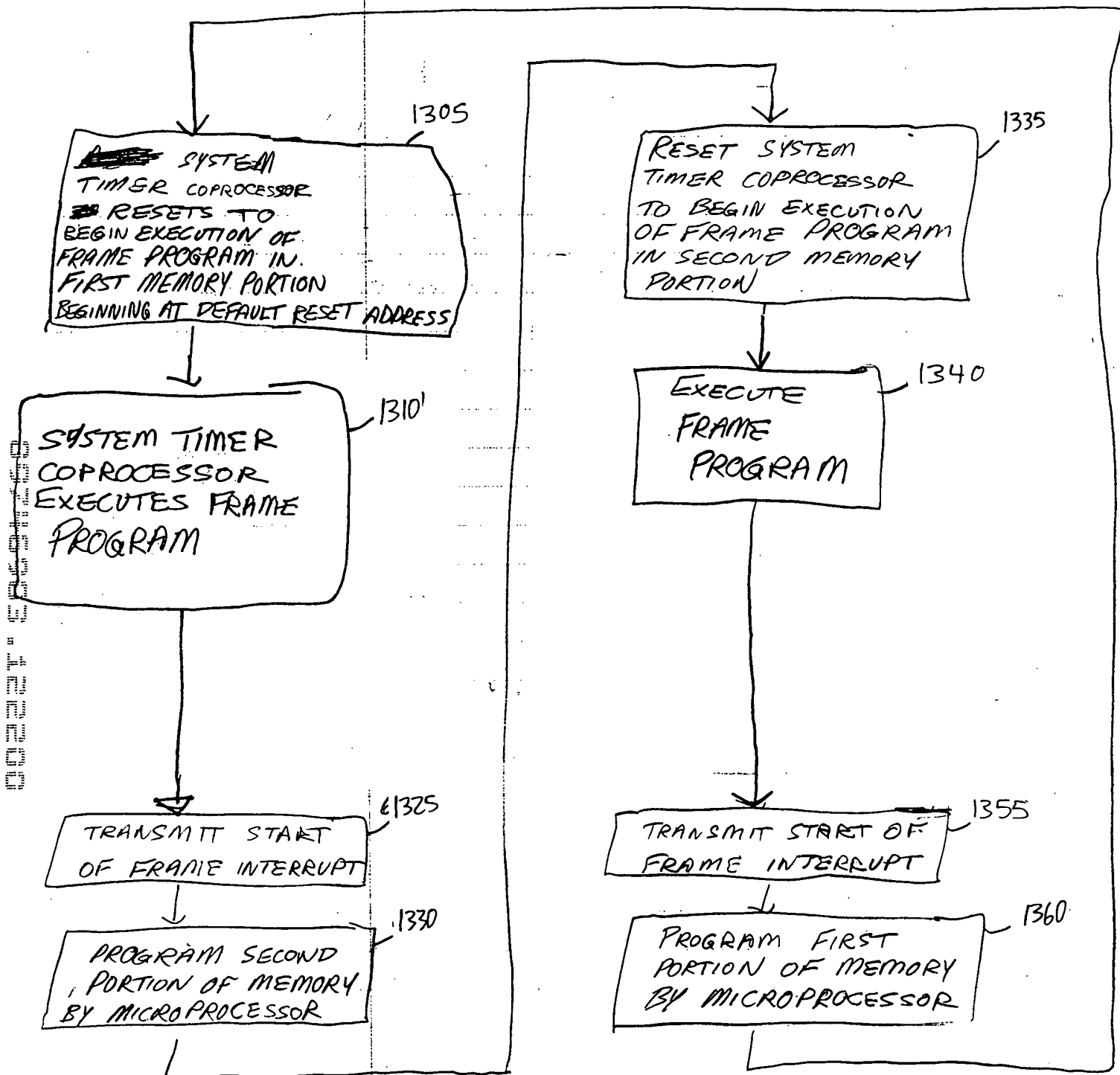


FIGURE 12